



SCH1435

N-Channel Power MOSFET 30V, 3A, 89mΩ, Single SCH6

ON Semiconductor®

<http://onsemi.com>

Features

- 1.8V drive
- Halogen free compliance
- Protection diode in

Specifications

Absolute Maximum Ratings at Ta=25°C

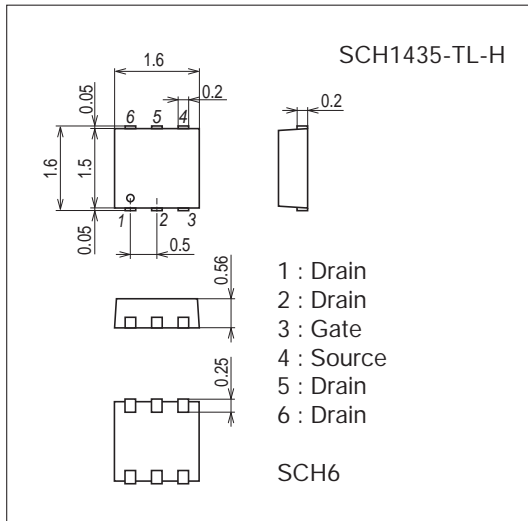
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		30	V
Gate-to-Source Voltage	V _{GSS}		±12	V
Drain Current (DC)	I _D		3	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	12	A
Allowable Power Dissipation	P _D	When mounted on ceramic substrate (900mm ² ×0.8mm)	0.8	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

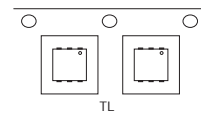
7028-002



Product & Package Information

- Package : SCH6
- JEITA, JEDEC : SOT-563
- Minimum Packing Quantity : 5,000 pcs./reel

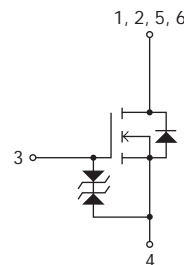
Packing Type : TL



Marking



Electrical Connection

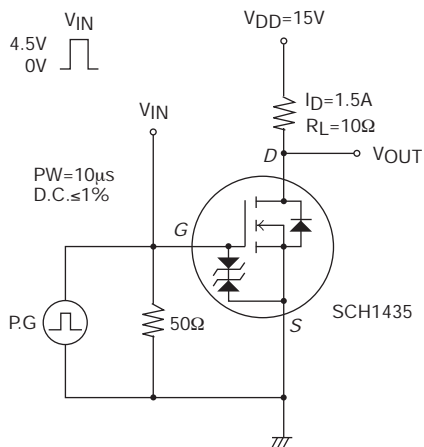


SCH1435

Electrical Characteristics at Ta=25°C

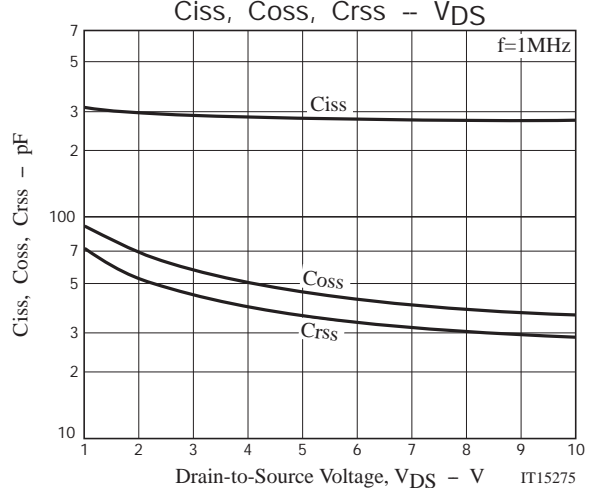
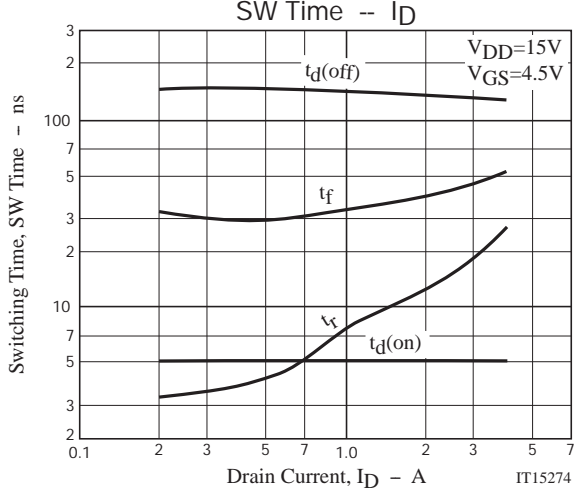
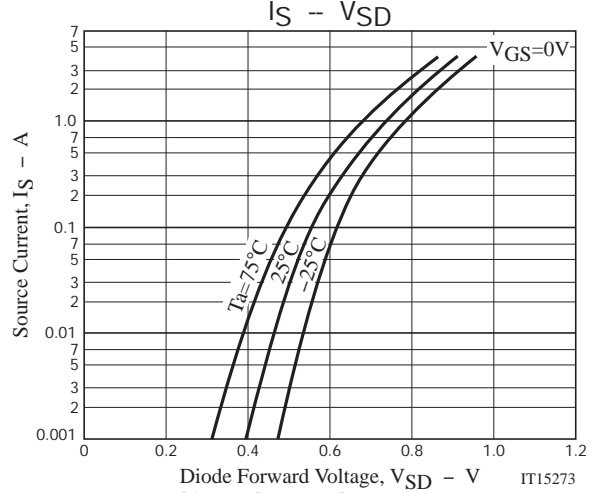
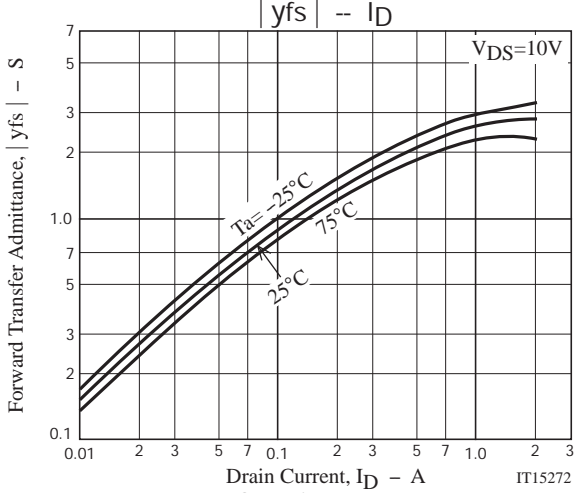
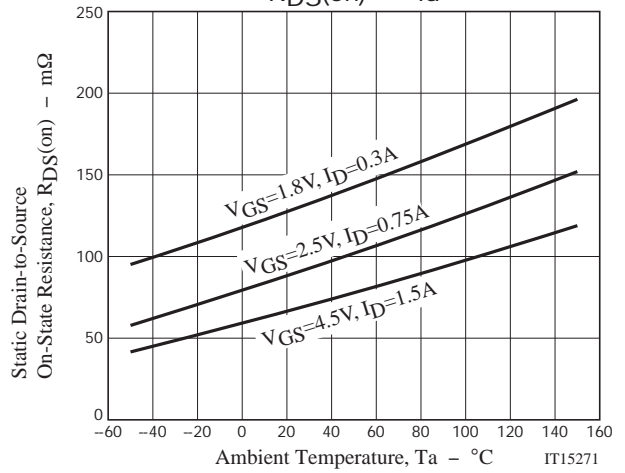
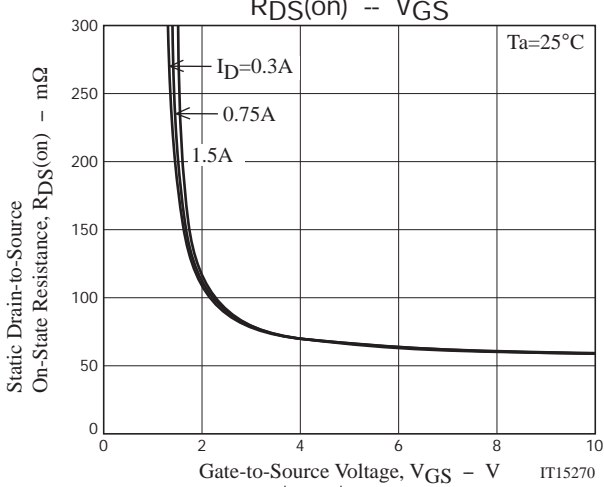
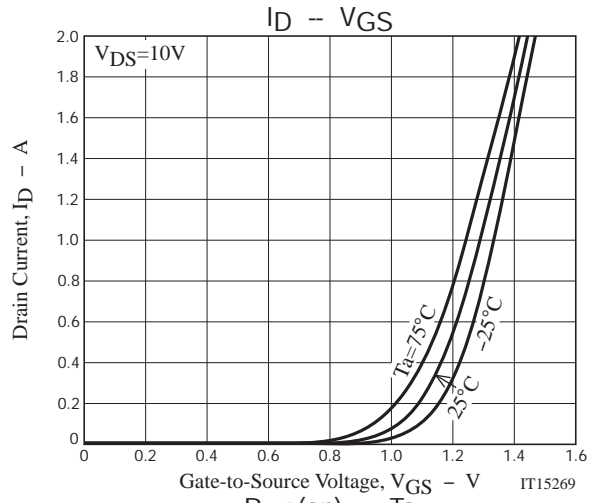
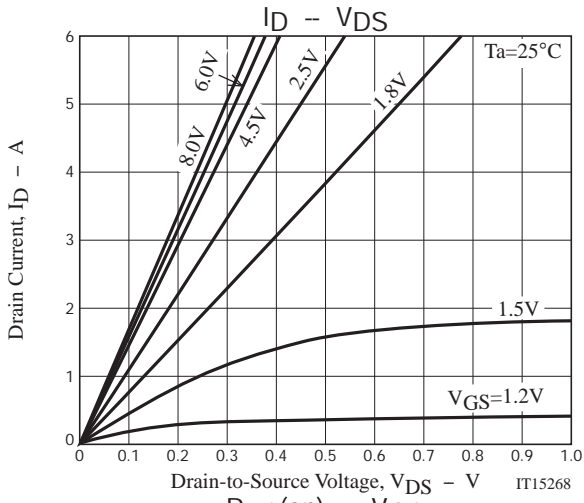
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0V	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, I _D =1.5A		2.7		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =1.5A, V _{GS} =4.5V		68	89	mΩ
	R _{DS(on)2}	I _D =0.75A, V _{GS} =2.5V		90	126	mΩ
	R _{DS(on)3}	I _D =0.3A, V _{GS} =1.8V		130	195	mΩ
Input Capacitance	C _{iss}	V _{DS} =10V, f=1MHz		265		pF
Output Capacitance	C _{oss}			35		pF
Reverse Transfer Capacitance	C _{rss}			28		pF
Turn-ON Delay Time	t _{d(on)}		See specified Test Circuit.		5.1	
Rise Time	t _r			10		ns
Turn-OFF Delay Time	t _{d(off)}			137		ns
Fall Time	t _f			36		ns
Total Gate Charge	Q _g	V _{DS} =15V, V _{GS} =4.5V, I _D =3A			3.5	
Gate-to-Source Charge	Q _{gs}			0.57		nC
Gate-to-Drain "Miller" Charge	Q _{gd}			0.93		nC
Diode Forward Voltage	V _{SD}	I _S =3A, V _{GS} =0V		0.87	1.2	V

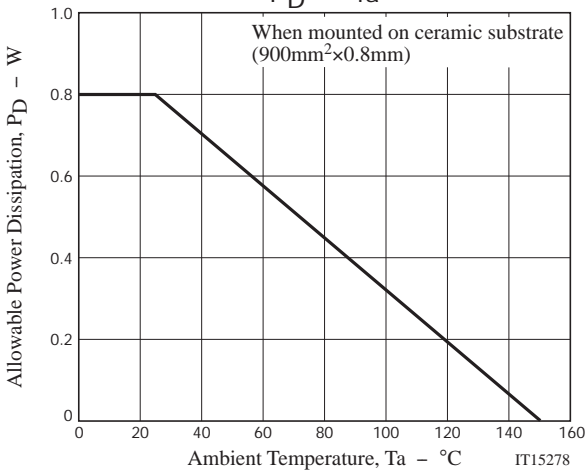
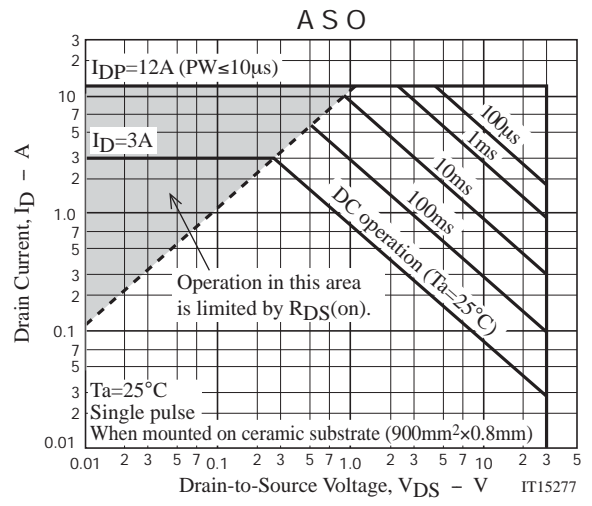
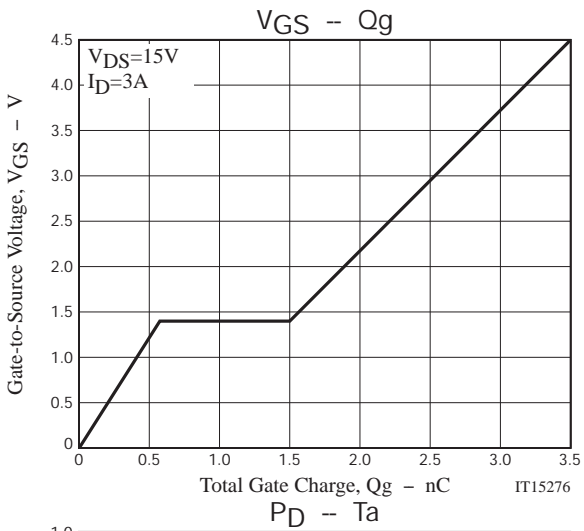
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
SCH1435-TL-H	SCH6	5,000pcs./reel	Pb Free and Halogen Free





Taping Specification

SCH1435-TL-H

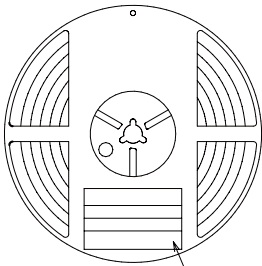
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
SCH6	SCH6	5,000	25,000	150,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Reel label, Inner box label
(unit:mm)

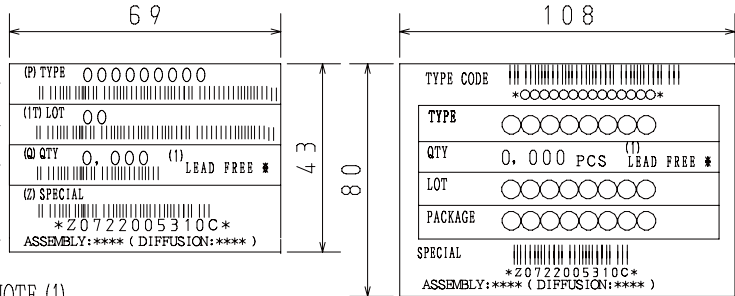
Outer box label
It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

Packing method



Type No.
LOT No.
Quantity
Origin

Reel label



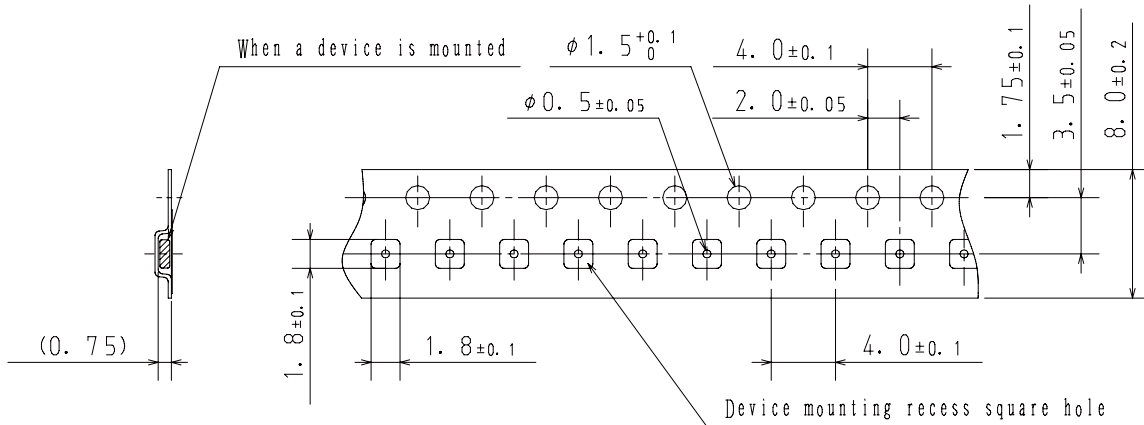
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

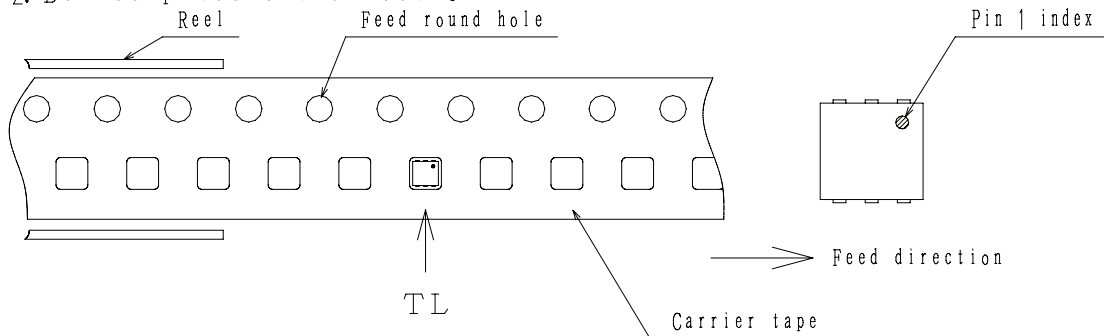
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



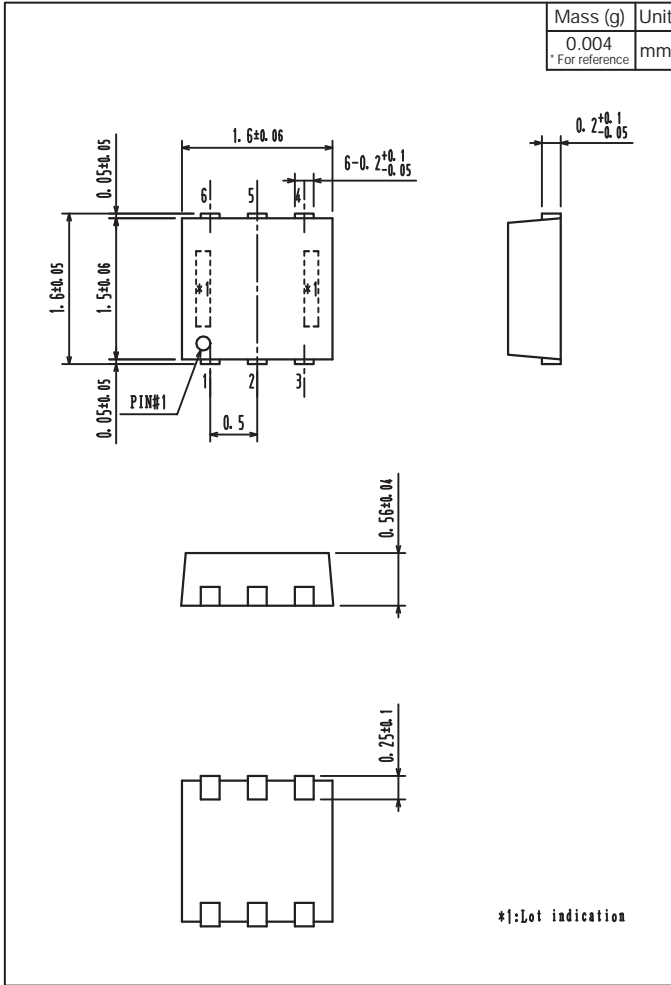
2-2. Device placement direction



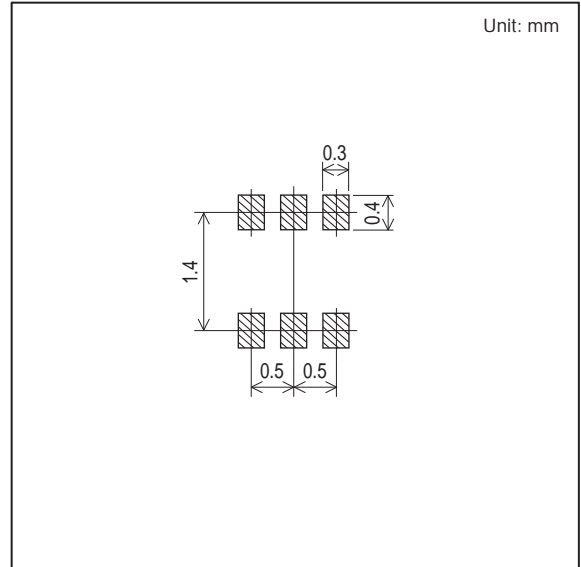
Those with pin 1 index on the feed hole side.....TL

SCH1435

Outline Drawing SCH1435-TL-H



Land Pattern Example



Note on usage : Since the SCH1435 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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